

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**APPLICANT:** Edmund J. BALBONI

**GROUP:** 2817

**SERIAL NO:** 10/679,023

**EXAMINER:** J. Chang

**FILED:** October 3, 2003

**CONFIRMATION:** 1507

**FOR: PHASE-LOCKED LOOP BANDWIDTH CALIBRATION CIRCUIT AND  
METHOD THEREOF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

**RESPONSE UNDER 37 C.F.R. 1.116**

In response to the Office Action mailed September 14, 2007, the following amendments and remarks are respectfully submitted under 37 C.F.R. 1.116 in connection with the above-identified application.